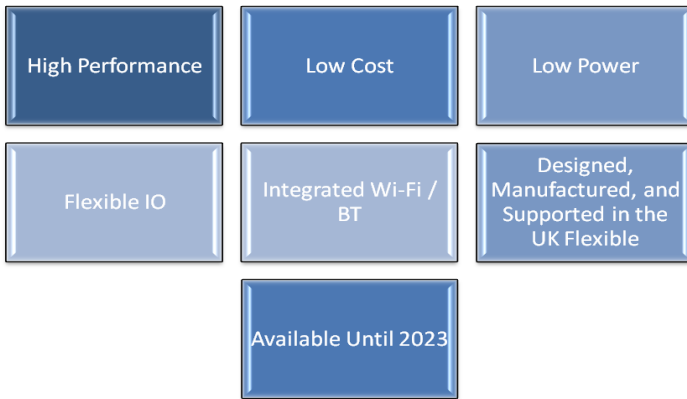
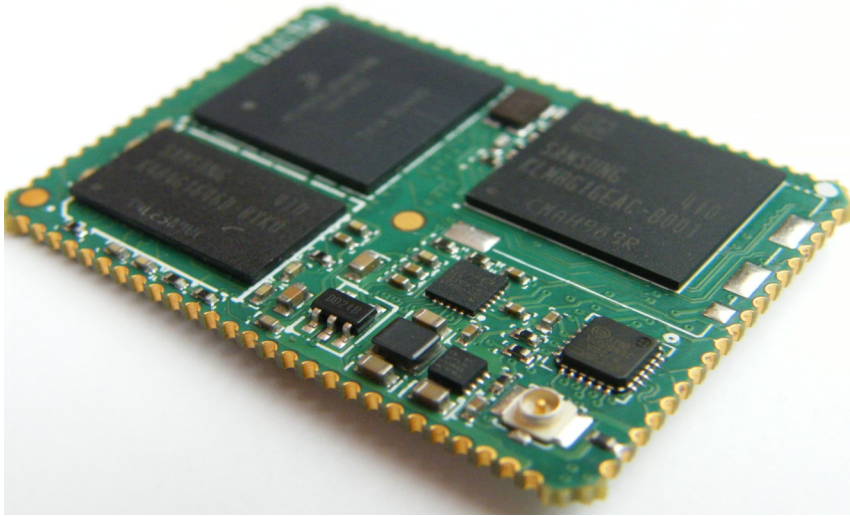


# MloTI -Module for Internet of Things Applications



## High Performance

MloTI features a Freescale i.MX6 ARM cortex A9 processing core with flexible clocking scheme from 24Mhz up to 1Ghz. This flexibility makes MloTI suitable for running high level operating systems like Ubuntu Linux and Android Lollipop, or running bare metal applications that only require lower frequencies .

## Low Power

Maximising battery life and reducing standby power are often important design requirements. MloTI is designed to operate from a 2.9V to 3.3V power supply making it suitable for battery operation. Dynamic frequency and voltage scaling (DVFS) is supported on the module to reduce power consumption when possible. The module also supports ultra lower power states (sub 75mW) while retaining memory contents.

## Low Cost

MloTI is competitively priced and is also designed reduce the total cost of a product. MloTI implements connectivity via gold pads on the edge of the board removing connector costs while also providing a more reliable connection to the host board. Wi-Fi is implemented with a layout to minimise / remove the requirement for R&TTE testing which can be costly. Typically only a 4-layer host board is required to implement a solution with MloTI. The module is suitable for SMT reflow soldering or hand soldering.

## Low Cost

MloTI measures only 38.1mm x 28mm

## Integrated connectivity

MloTI features 10/100 Ethernet including Physical layer for high speed wired networking. For wireless networking , Wi-Fi connectivity is provided with support for 802.11 A / B / G / N standards in both the 2,4Ghz and 5Ghz bands. Both station and access point modes are supported. The module also includes Bluetooth / BLE connectivity. A USB 2.0 Device and USB 2.0 Host port supporting HS / FS / and LS operation are available.

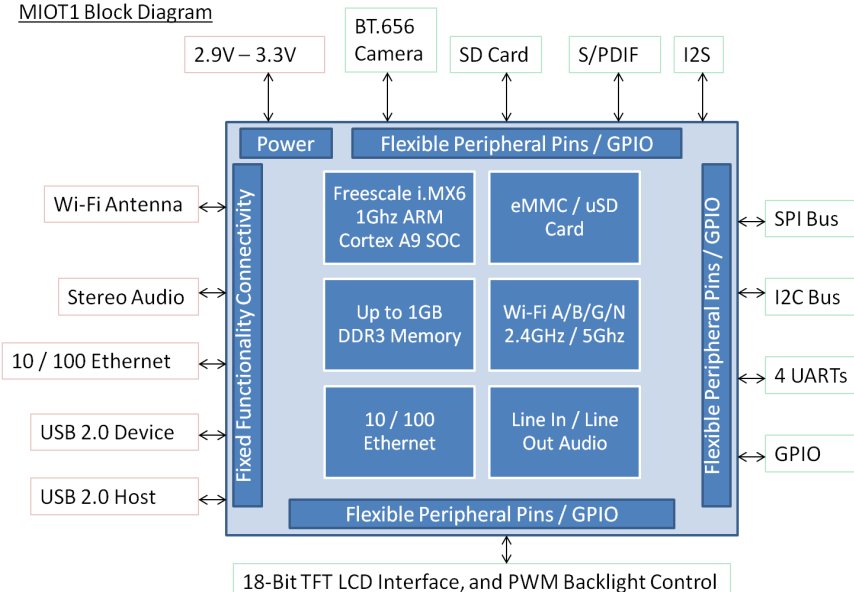
## Integrated storage

MloTI has the option of  $\mu$ SD card storage or soldered down eMMC memory. Both interfaces support high speed operation at 832 Mbps in UHS-I mode, with theoretical capacities up to 2TB.

## Software support

MloTI supports Embedded Linux, and Android Lollipop. Support for Microsoft WEC2013, and FreeRTOS is planned.

MIOT1 Block Diagram



\*Fixed Functionality pins

\* Flexible peripheral pins can be configured as GPIOs or as part of a hardware peripherals under software control.



# MloTI -Module for Internet of Things Applications

## Specification

### Standard Functions

<b>CPU</b>	Freescale i.MX6 Solo Lite 1Ghz Cortex A 9 with NEON co-processor	
<b>Memory</b>	256MB - 1024MB DDR3	
<b>Storage</b>	UHS-I uSD Card \ UHS-I eMMC	
<b>Connectivity</b>	<b>Ethernet:</b> <ul style="list-style-type: none"> <li>• 10 / 100 Mb,</li> <li>• Supports Auto MDIX</li> <li>• Physical layer implemented on module</li> </ul>	<b>USB:</b> <ul style="list-style-type: none"> <li>• USB Device - HS and FS modes</li> <li>• USB Host - Hs, FS, and LS modes</li> </ul>
	<b>Bluetooth:</b> <ul style="list-style-type: none"> <li>• Bluetooth 4.1</li> <li>• Bluetooth BLE 4.0</li> <li>• U.FL antenna connector on module or chip antenna on host board.*</li> </ul>	<b>Wi-Fi:</b> <ul style="list-style-type: none"> <li>• 802.11 A/B/G/N</li> <li>• 2.4Ghz / 5Ghz</li> <li>• Station and access points</li> <li>• U.FL antenna connector on module or chip antenna on host board.*</li> </ul>
	<p>* If a chip antenna is implemented on the host board as per the TI reference design, R&amp;TTE requirements can be minimised or avoided. R&amp;TTE consideration and testing is the responsibility of the host board designer.</p>	
<b>Connectivity</b>	<b>Audio:</b> <ul style="list-style-type: none"> <li>• On module Audio Codec</li> <li>• Headphone Output</li> <li>• Line input</li> </ul>	<b>Peripheral Pins:</b> <ul style="list-style-type: none"> <li>• Up to 60 General purpose inputs or outputs.</li> <li>• All peripheral pins have software controllable voltage levels of 1.8v or 3.3v</li> <li>• Peripheral pins can be GPIOs or form part of dedicated hardware interfaces.</li> </ul>
	<p>Supported hardware interfaces**:</p> <ul style="list-style-type: none"> <li>• 18bit TFT LCD interface with HS/VIS/DE LCD power control, and PWM backlight control.</li> <li>• 4 X UART operating up to 4Mhz</li> <li>• 1 X SPI port with chip selects</li> <li>• 1 X I2C bus</li> <li>• 1X SDIO</li> <li>• 1X BT.656 camera interface</li> <li>• S/PDIF Audio</li> <li>• I2S Audio</li> </ul> <p>** The module has been design to allow at least the above interfaces to be available on the module. Other interfaces can be made available if some of the above interfaces are not required for a particular design. E.g. if the camera interface is not required, an addition SDIO interface, or external i2S interface, or SPDIF interface can be implemented.</p>	
<b>Power</b>	Input range - 2.9v - 3.3v	
<b>Power Consumption</b>	S3 suspend to RAM - 74mW. Idle - Ubuntu desktop - 480mW.	
<b>Operating Environment</b>	Standard: 0-70, Extended: -40 -85	
<b>Dimensions</b>	38.1mm x 27.94mm x 5.3mm	